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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/077,189	02/15/2002	Tong Liu	ACT-322	2718
7590 09/21/2005				
Michael A. Blake Sierra Patent Group, Ltd. P.O. Box 6149 Stateline, NV 89449		EXAMINER THOMPSON, ANNETTE M		
		ART UNIT 2825 PAPER NUMBER		
DATE MAILED: 09/21/2005				

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/077,189

Applicant(s)

LIU ET AL.

Examiner

A. M. Thompson

Art Unit

2825

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 29 June 2005.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-7 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-7 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☒ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 25 June 2004 is/are: a) ☐ accepted or b) ☒ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input checked="" type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Continued Examination Under 37 CFR 1.114

1. A request for continued examination under 37 CFR 1.114, including the fee set forth in 37 CFR 1.17(e), was filed in this application after final rejection. Since this application is eligible for continued examination under 37 CFR 1.114, and the fee set forth in 37 CFR 1.17(e) has been timely paid, the finality of the previous Office action has been withdrawn pursuant to 37 CFR 1.114. Applicants' submission filed on 29 June 2005 has been entered.
2. Applicants' amendment filed 18 February 2005 to application 10/077,189 has been examined and remarks reviewed. The specification is amended. Claims 1-7 are pending.
3. Applicants' amendment has been fully considered but is not deemed persuasive. The applicable rejections from the prior office action are incorporated herein.

Specification

4. Applicant is reminded of the proper language and format for an abstract of the disclosure.

The abstract should be in narrative form and generally limited to a single paragraph on a separate sheet within the range of 50 to 150 words. It is important that the abstract not exceed 150 words in length since the space provided for the abstract on the computer tape used by the printer is limited. The form and legal phraseology often used in patent claims, such as "means", "said," and comprising should be avoided. The abstract should describe the disclosure sufficiently to assist readers in deciding whether there is a need for consulting the full patent text for details.

The language should be clear and concise and should not repeat information given in the title. It should avoid using phrases which can be implied, such as, "The disclosure concerns," "The disclosure defined by this invention," "The disclosure describes," etc.

5. The abstract of the disclosure is objected to because it contains claim language and is non-narrative. At line 3, change "having" to - has- -. At line 6, change "filed" to - field- -. At line 6, after "set", insert - of- -. At line 8, delete "said". At line 8, after "intersections" an article is needed. Correction is required. See MPEP § 608.01(b).

Claim Rejections - 35 USC § 102

6. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

Rejection of claims 1-7

7. Claims 1-7 are rejected under 35 U.S.C. 102(e) as being anticipated by Kaptanoglu, U.S. Patent 6,285,212. Kaptanoglu discloses a FPGA architecture.

8. Pursuant to claim 1, Kaptanoglu discloses a freeway routing system comprising a plurality of FPGA tiles comprising a plurality of functional groups arranged in rows and columns (Fig. 2; col. 2, ll. 28-37; col. 3, ll. 1-12); a plurality of interface groups surrounding the plurality of FGs (col. 2, ll. 30-33; Fig. 1, #14) such that one IG is positioned at each end of each row and column; a first set of routing conductors configured to transfer signals (col. 2, ll. 28-37); said first set of routing conductors comprising a plurality of vertical conductors that form intersections with a plurality of horizontal conductors and programmable interconnect elements (col. 6, line 60 to col. 7,

Art Unit: 2825

line 16) located at said intersections in a diagonal orientation on said FPGA tile (col. 8, line 66 to col. 9, line 4; col. 10, ll. 6-21).

9. Pursuant to claim 2, further comprising a second set of routing conductors configured to transfer signals. . . (Fig. 1, illustrates multiple FPGA tiles).

10. Pursuant to claim 3, further comprising programmable interconnect elements (col. 6, ll. 5-19, the F-tab; col. 7, ll. 4-16, the E-turn) located at the connections between adjacent FPGA tiles.

11. Pursuant to claim 4, wherein the diagonally oriented programmable interconnects are arranged from the upper left corner of a FPGA tile to the lower right corner of the FPGA tile (Fig. 6, #60; col. 10, ll. 51-67; col. 11, ll. 1-22).

12. Pursuant to claim 5, wherein the diagonally oriented programmable interconnects are arranged from the upper right corner of a FPGA tile to the lower left corner of the FPGA tile (Fig. 6, #6; col. 10, line 35 to col. 11, line 22).

13. Pursuant to claim 6, wherein the freeway set of routing conductors are configured to transfer signals from output ports of one IG (col. 4, ll. 31-43; col. 11, ll. 1-22).

14. Pursuant to claim 7, wherein the freeway set of routing conductors are configured to transfer signals from output ports of one RAM (col. 11, ll. 7-22).

Response to Remarks

15. Applicants' revised abstract is not approved and requires further amendment. Applicants' may peruse other patent abstract for examples of generally acceptable narrative formats. For example, review the abstract of Kaptanoglu.

16. Contrary to Applicants' assertion, Examiner has not "missed the gist" of Applicants' arguments. Examiner has meticulously mapped Applicants' claimed limitations to the Kaptanoglu prior art. In response, Applicants have made no amendments that change the scope of the claims in a manner that overcomes the prior art of Kaptanoglu. Kaptanoglu discloses all the elements of Applicants' claim 1 as cited, supra. Examiner suggests that Applicants schedule a telephone interview in this matter as without persuasive claim amendments, it is unlikely that this application will ever be allowed.

Conclusion

17. Any inquiry concerning this communication or earlier communications should be directed to Examiner A.M. Thompson whose telephone number is (571) 272-1909. The Examiner can usually be reached Monday thru Friday from 8:00 a.m. to 4:30 p.m..

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Responses to this action should be mailed to the appropriate mail stop:

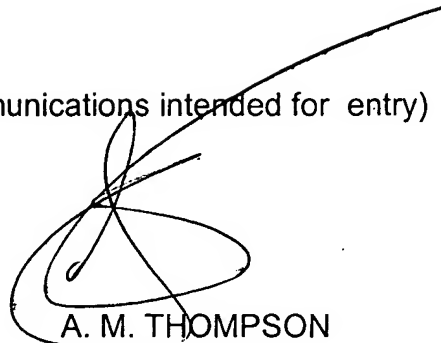
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P.O. Box 1450

Application/Control Number: 10/077,189
Art Unit: 2825

Page 6

Alexandria, VA 22313-1450
or faxed to:

(571) 273-8300, (for all **OFFICIAL** communications intended for entry)



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